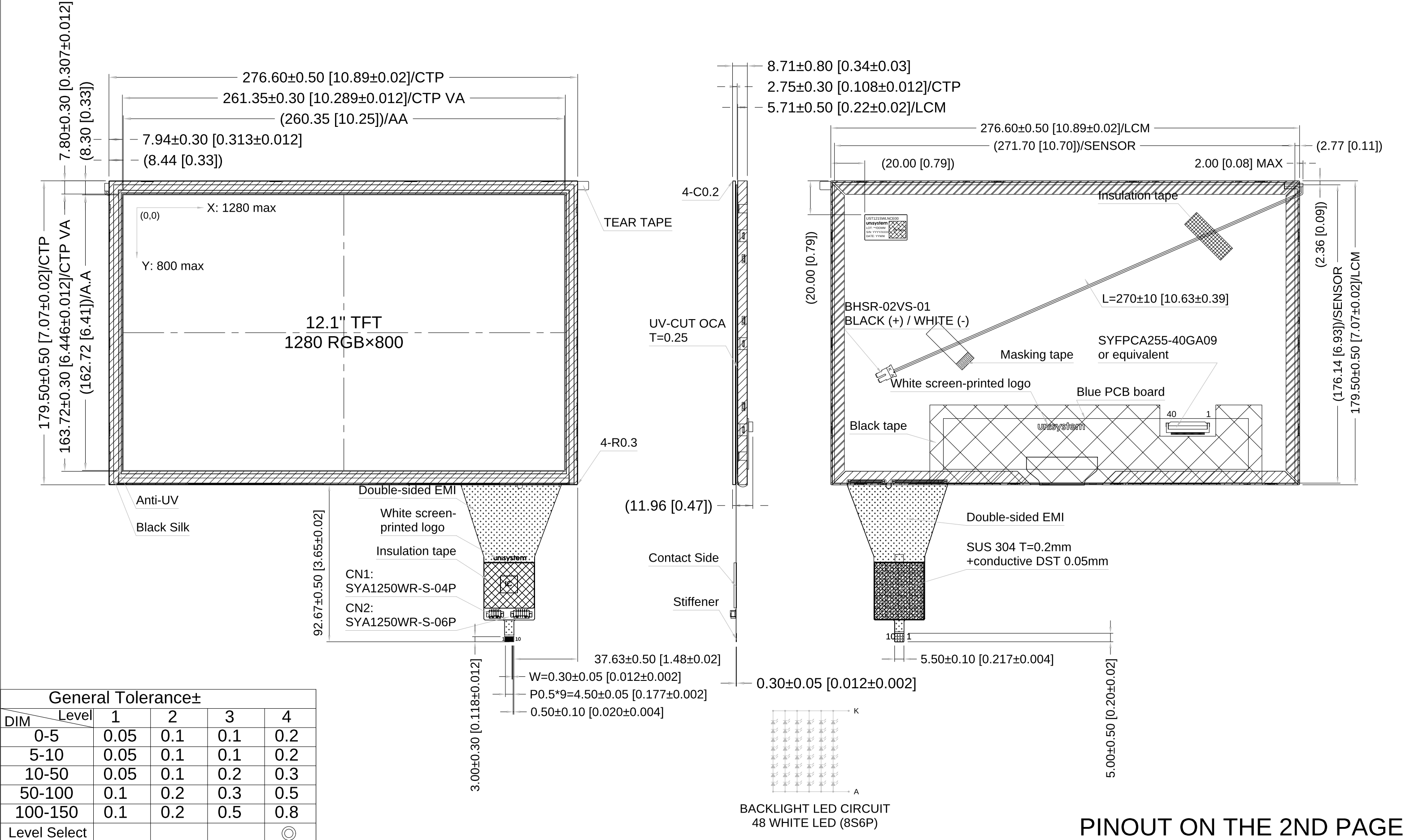




General Tolerance±					
DIM \ Level	1	2	3	4	
0-5	0.05	0.1	0.1	0.2	
5-10	0.05	0.1	0.1	0.2	
10-50	0.05	0.1	0.2	0.3	
50-100	0.1	0.2	0.3	0.5	
100-150	0.1	0.2	0.5	0.8	
Level Select					◎

TFT NOTES:

1. DISPLAY TYPE: TRANSMISSIVE, IPS, NORMALLY BLACK
2. DISPLAY SIZE: 12.1"
3. RESOLUTION: 1280x800
4. VIEWING ANGLE: FULL
5. INTERFACE: LVDS
6. LOGIC VOLTAGE: 3.3V
7. SURFACE LUMINANCE: 900 cd/m² (TYP., WITH CTP)
8. BACKLIGHT: 48 WHITE LED (8S6P)
Vf= 21.6~25.6V, If= 360mA, LED LIFETIME: 100k hours.

TP NOTES:

1. TP STRUCTURE: G+G
2. INTERFACE: USB/I2C
3. DRIVER IC: ILI2520
4. OPERATING VOLTAGE: 3.3V(I2C); 5.0V(USB)
5. SURFACE HARDNESS: ≥ 6H
6. TRANSMITTANCE: ≥ 85%
7. CG GLASS THICKNESS: 1.8mm

GENERAL NOTES:

1. OPERATING TEMPERATURE: -20°C ~ 70°C
2. STORAGE TEMPERATURE: -30°C ~ 80°C
3. WITHOUT INDIVIDUAL TOLERANCE: SEE TABLE
4. RoHS COMPLIANT
5. MAX. FLATNESS GEOMETRIC TOLERANCE OF THE SURFACE ON WHICH THE MODULE IS MOUNTED: 0.5
6. MIN.SURFACE ENERGY OF THE MATERIAL ON WHICH THE MODULE IS MOUNTED: 37 mN/m(dyn/cm)
7. MAX. FFC PULLING FORCE: TBD
8. SHELF TIME OF THE MODULE: 2 YEARS.
9. () - REFERENCE DIM; [] - INCH DIM.

PN:UST121SWLNCE00			
REV/DOC_VER: V2 / D1			
DRAWN: CU	12.10.2026	1:2	
CHECKED: J.M.	12.10.2026	[mm]	
APPR: J.M.	12.10.2026	ISO A3	

P. 1 of 2

unisystem

LCD CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	NC	21	RIN3+
2	VDD	22	GND
3	VDD	23	NC
4	VDD	24	NC
5	NC	25	GND
6	GND	26	NC
7	GND	27	NC
8	RIN0-	28	GND
9	RIN0+	29	NC
10	GND	30	NC
11	RIN1-	31	GND
12	RIN1+	32	GND
13	GND	33	GND
14	RIN2-	34	NC
15	RIN2+	35	NC
16	GND	36	NC
17	LVDS_CLK-	37	NC
18	LVDS_CLK+	38	NC
19	GND	39	NC
20	RIN3-	40	NC

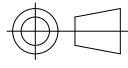
TOUCH PANEL CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	USB_GND	6	I2C_VDD
2	USB_VDD	7	I2C_RST
3	USB_D-	8	I2C_SCL
4	USB_D+	9	I2C_INT
5	I2C_GND	10	I2C_SDA

BACKLIGHT CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	LED+ (BLACK)	2	LED- (WHITE)

CN1 CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	USB_VDD	3	USB_D+
2	USB_D-	4	USB_GND

CN2 CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	I2C_GND	4	I2C_SCL
2	I2C_VDD	5	I2C_INT
3	I2C_RST	6	I2C_SDA

V2	D1	LCD PCB updated	12/10/2026
V1	D1	Initial Case	10/10/2026
Rev	Ver	Changes	Data

			unisystem			
			DRAWN: CU	12.10.2026	1:2	
			CHECKED: J.M.	12.10.2026	[mm]	
			APPR: J.M.	12.10.2026	ISO A3	