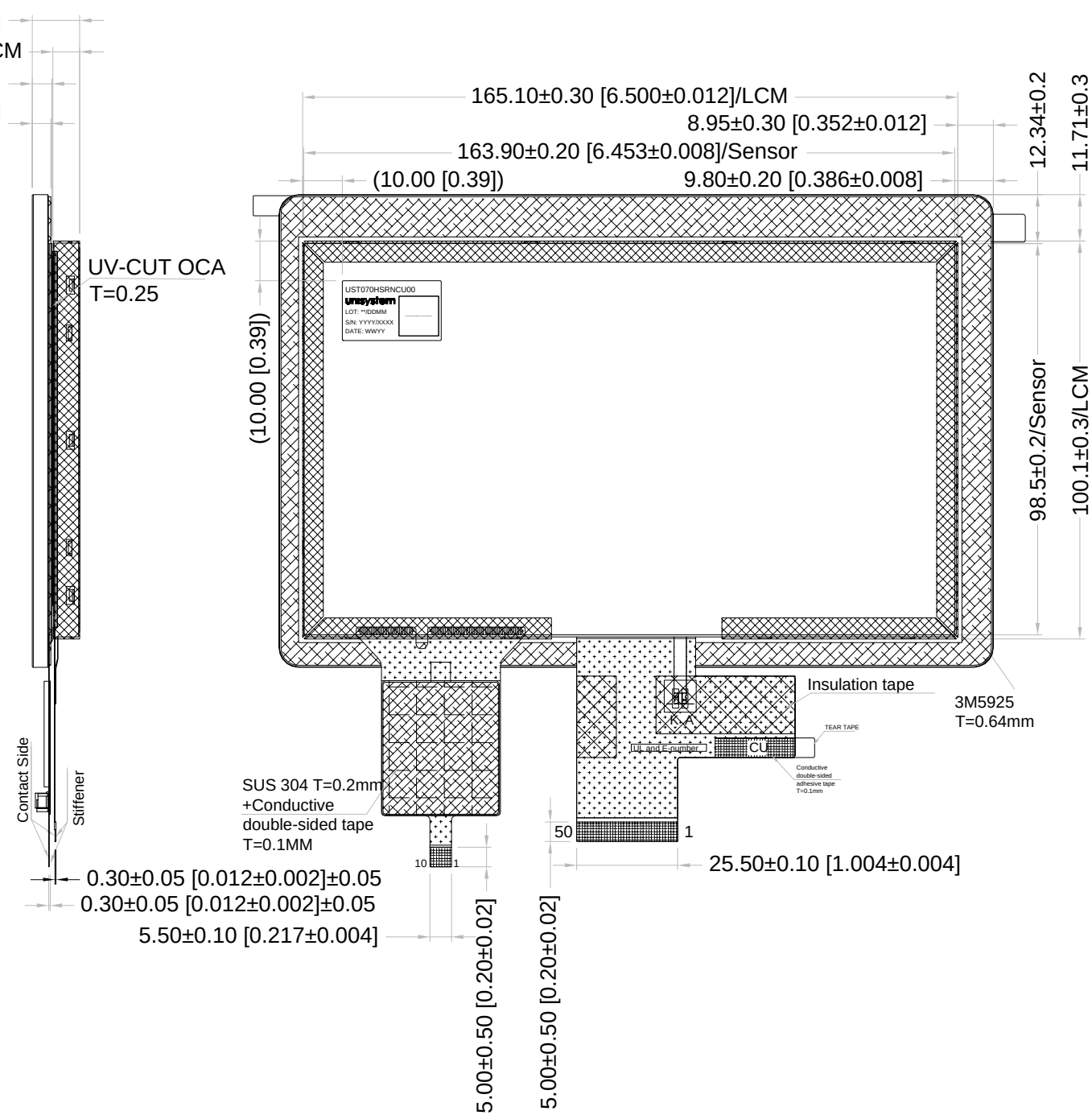
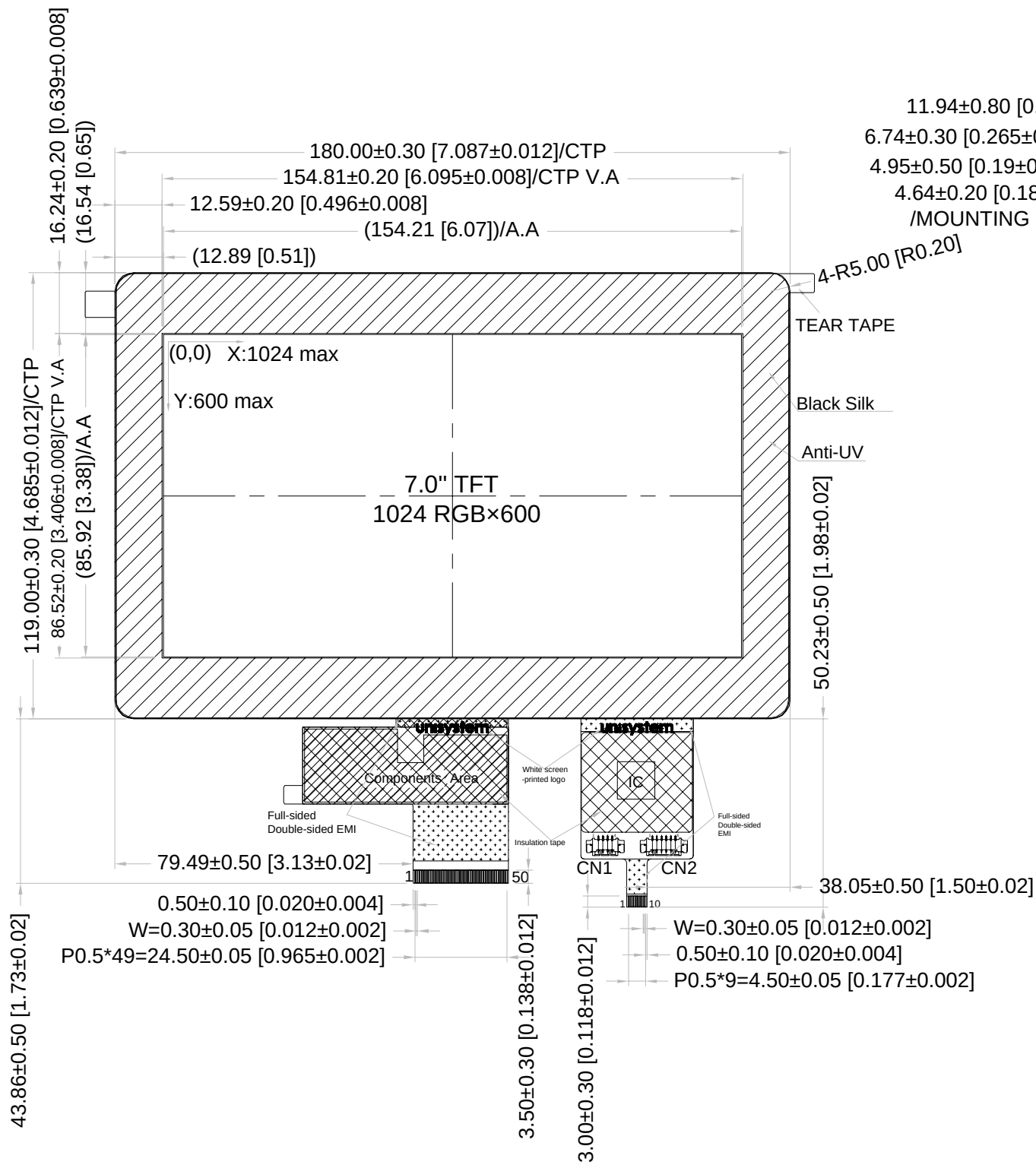
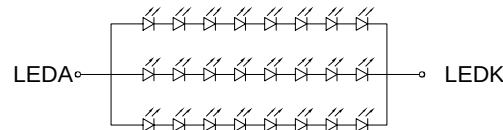




General Tolerance±					
DIM	Level	1	2	3	4
0-5		0.05	0.1	0.1	0.2
5-10		0.05	0.1	0.1	0.2
10-50		0.05	0.1	0.2	0.3
50-100		0.1	0.2	0.3	0.5
100-150		0.1	0.2	0.5	0.8
Level Seclect					◎



PINOUT ON THE 2ND PAGE

TFT NOTES: 1. DISPLAY TYPE: TRANSMISSIVE, NORMALLY BLACK, GLARE 2. COLOR DEPTH: 16.7M 3. RESOLUTION: 1024x600 4. VIEWING ANGLE: FULL 5. INTERFACE: RGB 6. DRIVER IC: JD9165BA 7. LOGIC VOLTAGE: 3.3V 8. SURFACE LUMINANCE: 1900 cd/m ² (WITH CTP) 9. BACKLIGHT: 24 WHITE LED/24(TPY) V _F = 21.6~26.4V I _F = 240mA LED LIFETIME: 100k hours.		TP NOTES: 1. TP STRUCTURE: G+G 2. INTERFACE: USB/I2C 3. DRIVER IC: ILI2520 4. OPERATING VOLTAGE: 3.3V(I2C); 5.0V(USB) 5. SURFACE HARDNESS: ≥6H 6. TRANSMITTANCE: ≥85% 7. CG GLASS THICKNESS: 4.0mm		GENERAL NOTES: 1. OPERATING TEMPERATURE: -20°C ~ 70°C 2. STORAGE TEMPERATURE: -30°C ~ 80°C 3. WITHOUT INDIVIDUAL TOLERANCE: SEE TABLE 4. RoHS COMPLIANT 5. MAX. FLATNESS GEOMETRIC TOLERANCE OF THE SURFACE ON WHICH THE MODULE IS MOUNTED: 6. MIN.SURFACE ENERGY OF THE MATERIAL ON WHICH THE MODULE IS MOUNTED: 37 mN/m(dyn/cm) 7. MAX. FFC PULLING FORCE: TBD 8. SHELF TIME OF THE MODULE: 2 YEARS. 9. () - REFERENCE DIM; [] - INCH DIM.		PN: UST070HSRNCU00 REV/DOC_VER: V2 / D1		unisystem			
DRAWN: M.C.		05.10.2026		1:1.55							
CHECKED: MP		05.10.2026		[mm]							
APPR: J.M.		05.10.2026		ISO A3						P. 1 of 2	

unisystem

LCD CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	LED+	26	G1
2	LED+	27	G0
3	LED-	28	R7
4	LED-	29	R6
5	GND	30	R5
6	NC	31	R4
7	VDD	32	R3
8	MODE	33	R2
9	DE	34	R1
10	VSYNC	35	R0
11	HSYNC	36	GND
12	B7	37	DCLK
13	B6	38	GND
14	B5	39	L/R
15	B4	40	U/D
16	B3	41	NC
17	B2	42	NC
18	B1	43	NC
19	B0	44	RSTB
20	G7	45	NC
21	G6	46	NC
22	G5	47	STBYB
23	G4	48	GND
24	G3	49	NC
25	G2	50	NC

TOUCH PANEL CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	USB_GND	6	I2C_VDD
2	USB_VDD	7	I2C_RST
3	USB_D-	8	I2C_SCL
4	USB_D+	9	I2C_INT
5	I2C_GND	10	I2C_SDA

CN1 CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	USB_VDD	3	USB_D+
2	USB_D-	4	USB_GND

CN2 CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	I2C_GND	4	I2C_SCL
2	I2C_VDD	5	I2C_INT
3	I2C_RST	6	I2C_SDA

V2	D1	Adjusted Design Details	31/08/2026
V1	D1	Initial Case	19/06/2026
Rev	Ver	Changes	Data

PN: UST070HSRNCU00

REV/VER: V2 / D1

DRAWN: M.C.	05.10.2026	1:1.55	
CHECKED: MP	05.10.2026	[mm]	
APPR: J.M.	05.10.2026	ISO A3	