



**WINSTAR Display Co.,Ltd.**  
**華凌光電股份有限公司**



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## SPECIFICATION

**CUSTOMER :** \_\_\_\_\_

**MODULE NO.:** WDO0066-TML-#04

<b>APPROVED BY:</b> ( FOR CUSTOMER USE ONLY )	<b>PCB VERSION:</b> _____ <b>DATA:</b> _____
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SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

VERSION	DATE	REVISED PAGE NO.	SUMMARY
A	2014/01/22		Increase the lifetime of the materials. Correct contour drawing.



RECORDS OF REVISION			DOC. FIRST ISSUE
VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2013/09/24		First issue
A	2014/01/22		Increase the lifetime of the materials. Correct contour drawing.

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# 1. Module Classification Information

W	D	O	0066	-	T	M	L	-	#04
①	②	③	④		⑤	⑥	⑦		⑧

① Brand : WINSTAR DISPLAY CORPORATION

② Custom : D

③ Display Type : H→ Character Type ; G→ Graphic Type; N → LCD Display; O→ COG Type

④ Model serials no.0000 - ZZZZ

⑤ Backlight Type :

N→Without backlight	T→LED, White	S→LED, High light White
B→EL, Blue green	A→LED, Amber	L→LED, Full color
D→EL, Green	R→LED, Red	J→DIP LED,Blue
W→EL, White	O→LED, Orange	K→DIP LED,White
M→EL, Yellow Green	G→LED, Green	E→DIP LED, Yellow Green
F→CCFL, White	P→LED, Blue	H→DIP LED,Amber
Y→LED, Yellow Green	X→LED, Dual color	I→DIP LED, Red
G→LED, Green	C→LED, Full color	

⑥ LCD Mode :

B→TN Positive, Gray	V→FSTN Negative, Blue
N→TN Negative,	T→FSTN Negative, Black
L→VA Negative	D→FSTN Negative (Double film)
H→ HTN Positive, Gray	F→FSTN Positive
I→HTN Negative, Black	K→FSC Negative
U→HTN Negative, Blue	S→FSC Positive
M→STN Negative, Blue	E→ISTN Negative, Black
G→STN Positive, Gray	C→CSTN Negative, Black
Y→STN Positive, Yellow Green	A→ASTN Negative, Black

⑦ LCD Polarizer Type/ Temperature range/ View direction

A→Reflective, N.T, 6:00	H→Transflective, W.T,6:00
D→Reflective, N.T, 12:00	K→Transflective, W.T,12:00
G→Reflective, W. T, 6:00	C→Transmissive, N.T,6:00
J→Reflective, W. T, 12:00	F→Transmissive, N.T,12:00
B→Transflective, N.T,6:00	I→Transmissive, W. T, 6:00
E→Transflective, N.T.12:00	L→Transmissive, W.T,12:00

⑧ Special Code

#:Fit in with the ROHS Directions and regulations

0:Sales Code

4:Version (Assigned LCD)

## **2.Precautions in use of LCD Modules**

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8) Winstar have the right to change the passive components, including R3,R6 & backlight adjust resistors. (Resistors,capacitors and other passive components will have different appearance and color caused by the different supplier.)
- (9) Winstar have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Winstar have the right to modify the version.)
- (10) There is lifetime limit on the following materials involved , there would be re-tool or low yield rate problem if exceed the period.

<b>Material type</b>	<b>frame</b>	<b>LCD</b>	<b>PCB</b>	<b>Backlight / light guide</b>	<b>Touch panel</b>	<b>Heat seal</b>
<b>lifetime</b>	2 years	2 years	2 years	2 years	1 year	nine months

### **3.General Specification**

<b>Item</b>	<b>Dimension</b>	<b>Unit</b>
Number of Characters	128*64	dots
Module dimension	75.8x 47.96 x6.3 (MAX)	mm
View area	64.0 x 35.5	mm
Active area	60.785 x 32.945	mm
Dot size	0.46 x0.50	mm
Dot pitch	0.475 x 0.515	mm
LCD type	STN,BLUE ,Transmisstive/Negative (In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.)	
Duty/ Bias	1/64 DUTY,1/9BIAS	
View direction	12 O'clock	
Backlight Type	LED White	
IC	ST7565P	

## 4. Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T <sub>OP</sub>	-20	—	+70	°C
Storage Temperature	T <sub>ST</sub>	-30	—	+80	°C
Power Supply Voltage	VDD	-0.3	—	3.6	V
Power supply voltage (VDD standard)	V0, VOUT	-0.3	—	14.5	V
Power supply voltage (VDD standard)	V1, V2, V3, V4	-0.3	—	V0+0.3	V



## **5. Electrical Characteristics**

<b>Item</b>	<b>Symbol</b>	<b>Condition</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>
Operating Voltage	VDD	V <sub>DD</sub> -V <sub>SS</sub>	3.0	3.3	3.6	V
Supply Voltage For LCD	V <sub>op</sub>	25°C	9.95	10.2	10.45	V

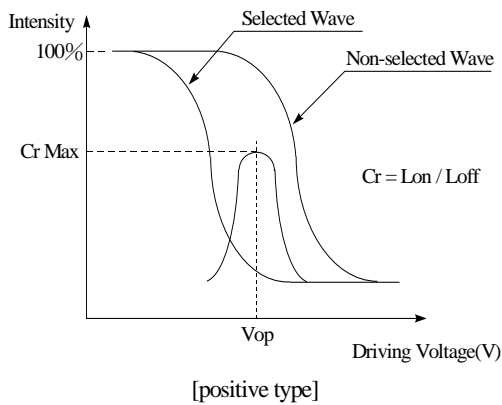
\*Another Data Please consult the spec of Sitronix ST7565P

Please kindly consider to design the V<sub>op</sub> to be adjustable while programming the software to match LCD contrast tolerance.

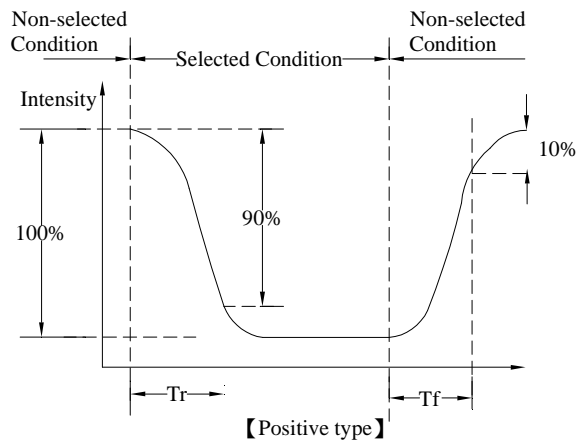
# 6. Optical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
View Angle	$\theta$	$CR \geq 2$	0	—	45	$\phi = 180^\circ$
	$\theta$	$CR \geq 2$	0	—	30	$\phi = 0^\circ$
	$\theta$	$CR \geq 2$	0	—	30	$\phi = 90^\circ$
	$\theta$	$CR \geq 2$	0	—	30	$\phi = 270^\circ$
Contrast Ratio	CR	—	—	10	15	—
Response Time	T rise	25°C	—	80	160	ms
	T fall	25°C	—	100	200	ms

## Definition of Operation Voltage (Vop)



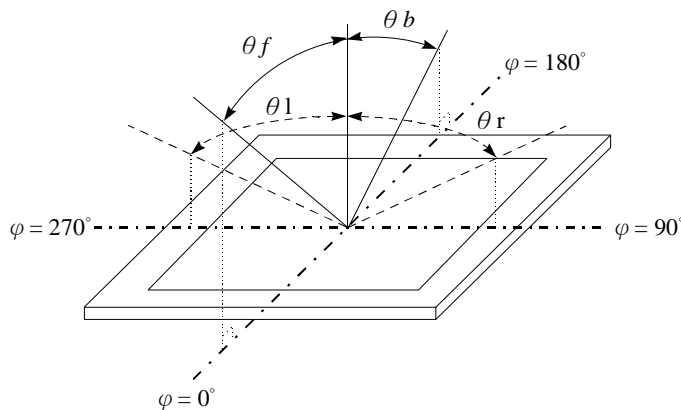
## Definition of Response Time (Tr, Tf)



## Conditions :

Operating Voltage : Vop      Viewing Angle( $\theta$  ,  $\phi$ ) :  $0^\circ$  ,  $0^\circ$   
 Frame Frequency : 64 HZ      Driving Waveform : 1/N duty , 1/a bias

## Definition of viewing angle( $CR \geq 2$ )

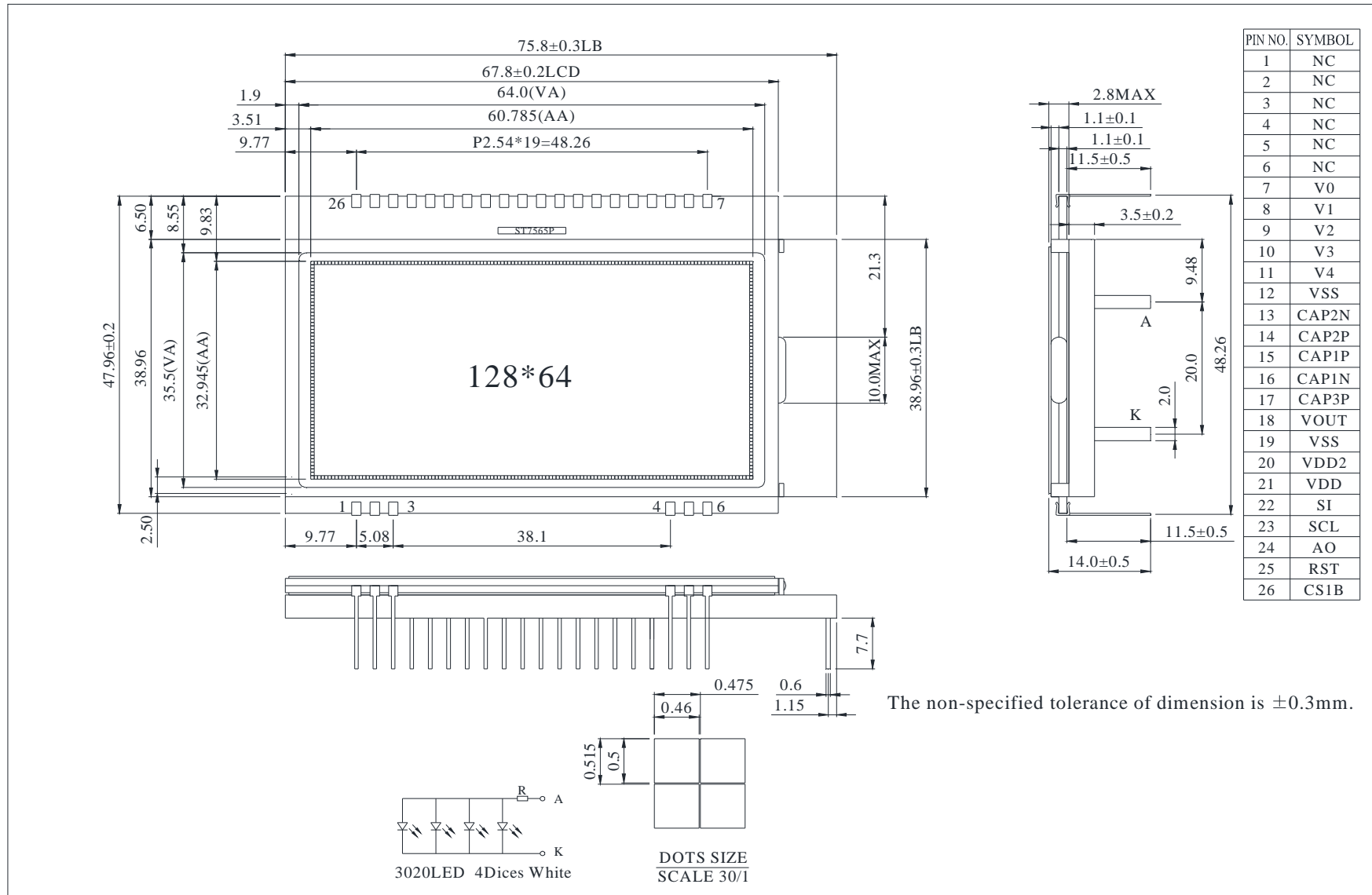


# 7. Interface Pin Function

Pin No.	Symbol	Description																														
1	NC	No connection																														
2	NC	No connection																														
3	NC	No connection																														
4	NC	No connection																														
5	NC	No connection																														
6	NC	No connection																														
7	V0	<p>This is a multi-level power supply for the liquid crystal drive. The voltage Supply applied is determined by the liquid crystal cell, and is changed through the use of a resistive voltage divided or through changing the impedance using an op. amp.</p> <p>Voltage levels are determined based on Vss, and must maintain the relative magnitudes shown below.</p> <p><math>V0 \geq V1 \geq V2 \geq V3 \geq V4 \geq Vss</math></p> <p>When the power supply turns ON, the internal power supply circuits produce the V1 to V4 voltages shown below. The voltage settings are selected using the LCD bias set command.</p> <table border="1"> <thead> <tr> <th></th> <th>1/65 DUTY</th> <th>1/49 DUTY</th> <th>1/33 DUTY</th> <th>1/55 DUTY</th> <th>1/53 DUTY</th> </tr> </thead> <tbody> <tr> <td>V1</td> <td>8/9*V0,6/7*V0</td> <td>7/8*V0,5/6*V0</td> <td>5/6*V0,4/5*V0</td> <td>7/8*V0,5/6*V0</td> <td>7/8*V0,5/6*V0</td> </tr> <tr> <td>V2</td> <td>7/9*V0,5/7*V0</td> <td>6/8*V0,4/6*V0</td> <td>4/6*V0,3/5*V0</td> <td>6/8*V0,4/6*V0</td> <td>6/8*V0,4/6*V0</td> </tr> <tr> <td>V3</td> <td>2/9*V0,2/7*V0</td> <td>2/8*V0,2/6*V0</td> <td>2/6*V0,2/5*V0</td> <td>2/8*V0,2/6*V0</td> <td>2/8*V0,2/6*V0</td> </tr> <tr> <td>V4</td> <td>1/9*V0,1/7*V0</td> <td>1/8*V0,1/6*V0</td> <td>1/6*V0,1/5*V0</td> <td>1/8*V0,1/6*V0</td> <td>1/8*V0,1/6*V0</td> </tr> </tbody> </table>		1/65 DUTY	1/49 DUTY	1/33 DUTY	1/55 DUTY	1/53 DUTY	V1	8/9*V0,6/7*V0	7/8*V0,5/6*V0	5/6*V0,4/5*V0	7/8*V0,5/6*V0	7/8*V0,5/6*V0	V2	7/9*V0,5/7*V0	6/8*V0,4/6*V0	4/6*V0,3/5*V0	6/8*V0,4/6*V0	6/8*V0,4/6*V0	V3	2/9*V0,2/7*V0	2/8*V0,2/6*V0	2/6*V0,2/5*V0	2/8*V0,2/6*V0	2/8*V0,2/6*V0	V4	1/9*V0,1/7*V0	1/8*V0,1/6*V0	1/6*V0,1/5*V0	1/8*V0,1/6*V0	1/8*V0,1/6*V0
	1/65 DUTY		1/49 DUTY	1/33 DUTY	1/55 DUTY	1/53 DUTY																										
V1	8/9*V0,6/7*V0		7/8*V0,5/6*V0	5/6*V0,4/5*V0	7/8*V0,5/6*V0	7/8*V0,5/6*V0																										
V2	7/9*V0,5/7*V0		6/8*V0,4/6*V0	4/6*V0,3/5*V0	6/8*V0,4/6*V0	6/8*V0,4/6*V0																										
V3	2/9*V0,2/7*V0		2/8*V0,2/6*V0	2/6*V0,2/5*V0	2/8*V0,2/6*V0	2/8*V0,2/6*V0																										
V4	1/9*V0,1/7*V0	1/8*V0,1/6*V0	1/6*V0,1/5*V0	1/8*V0,1/6*V0	1/8*V0,1/6*V0																											
8	V1																															
9	V2																															
10	V3																															
11	V4																															
12	Vss																															
13	CAP2N	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2P terminal.																														
14	CAP2P	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2N terminal.																														
15	CAP1P	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal.																														
16	CAP1N	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1P terminal.																														
17	CAP3P	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal.																														

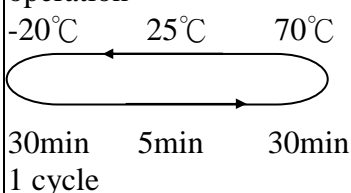
18	Vout	DC/DC voltage converter. Connect a capacitor between this terminal and VSS or VDD
19	Vss	Ground
20	VDD2	Power supply
21	VDD	Power supply
22	SI	Serial data input
23	SCL	Serial clock input
24	AO	This is connect to the least significant bit of the normal MPU address bus, and it determines whether the data bits are data or command. AO = "H": Indicates that D0 to D7 are display data. AO = "L": Indicates that D0 to D7 are control data.
25	RST	When /RES is set to "L", the register settings are initialized (cleared). The reset operation is performed by the /RES signal level.
26	CS1B	This is the chip select signal.

# 8. Contour Drawing



# 9. Reliability

Content of Reliability Test (Wide temperature, -20°C~70°C)

Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1
High Temperature/ Humidity storage	The module should be allowed to stand at 60°C, 90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60°C, 90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation  30min 5min 30min 1 cycle	-20°C/70°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V, RS=1.5kΩ CS=100pF 1 time	—

**Note1: No dew condensation to be observed.**

**Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.**

**Note3: The packing have to including into the vibration testing.**

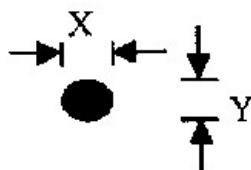
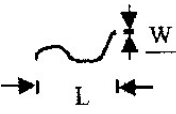
# 10.Backlight Information

## Specification

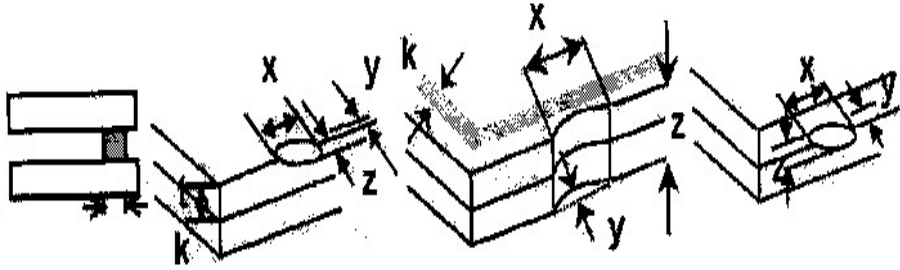
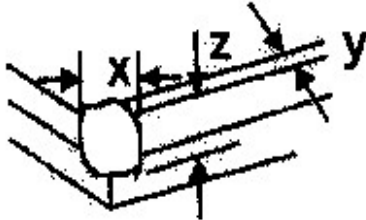
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Forward Voltage	Vf	3.2	3.3	3.4	V	If=45mA
Reverse Current	Ir	—	—	40	$\mu A$	Vr=5V
Reverse Voltage	VR	—	—	5	V	—
Luminous	LV	700	875	1100	CD/M <sup>2</sup>	If=45mA
Wavelength	$\lambda d$	—	—	—	nm	—
Color Coordinate	x	0.27±0.02			nm	If=45mA
	y	0.29±0.02				
Life Time	—	—	50000	—	H	If=45mA
Color	White					

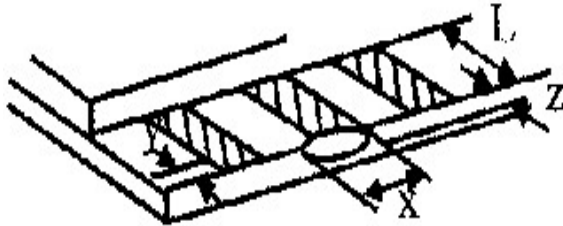
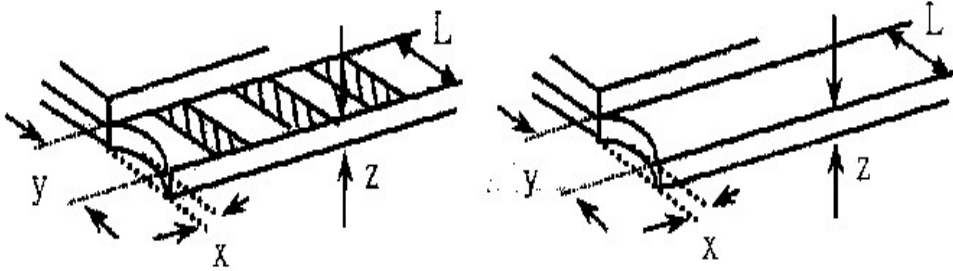
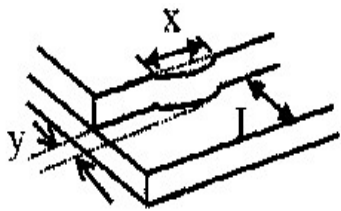
**Note:** The LED of B/L is drive by current only ; driving voltage is only for reference  
To make driving current in safety area (waste current between minimum and maximum).

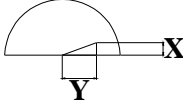
# 11. Inspection specification

NO	Item	Criterion	AQL													
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character , dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.	0.65													
02	Black or white spots on LCD (display only)	2.1 White and black spots on display $\leq 0.25\text{mm}$ , no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm	2.5													
03	LCD black spots, white spots, contamination (non-display)	3.1 Round type : As following drawing $\Phi = (x + y) / 2$  <table border="1" data-bbox="805 981 1332 1236"> <thead> <tr> <th>SIZE</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.10</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>0.10 &lt; \Phi \leq 0.20</math></td> <td>2</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.25</math></td> <td>1</td> </tr> <tr> <td><math>0.25 &lt; \Phi</math></td> <td>0</td> </tr> </tbody> </table>	SIZE	Acceptable Q TY	$\Phi \leq 0.10$	Accept no dense	$0.10 < \Phi \leq 0.20$	2	$0.20 < \Phi \leq 0.25$	1	$0.25 < \Phi$	0	2.5			
		SIZE	Acceptable Q TY													
$\Phi \leq 0.10$	Accept no dense															
$0.10 < \Phi \leq 0.20$	2															
$0.20 < \Phi \leq 0.25$	1															
$0.25 < \Phi$	0															
3.2 Line type : (As following drawing)  <table border="1" data-bbox="699 1328 1332 1579"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>---</td> <td><math>W \leq 0.02</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>L \leq 3.0</math></td> <td><math>0.02 &lt; W \leq 0.03</math></td> <td rowspan="2">2</td> </tr> <tr> <td><math>L \leq 2.5</math></td> <td><math>0.03 &lt; W \leq 0.05</math></td> </tr> <tr> <td>---</td> <td><math>0.05 &lt; W</math></td> <td>As round type</td> </tr> </tbody> </table>	Length	Width	Acceptable Q TY	---	$W \leq 0.02$	Accept no dense	$L \leq 3.0$	$0.02 < W \leq 0.03$	2	$L \leq 2.5$	$0.03 < W \leq 0.05$	---	$0.05 < W$	As round type	2.5	
Length	Width	Acceptable Q TY														
---	$W \leq 0.02$	Accept no dense														
$L \leq 3.0$	$0.02 < W \leq 0.03$	2														
$L \leq 2.5$	$0.03 < W \leq 0.05$															
---	$0.05 < W$	As round type														
04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.	<table border="1" data-bbox="826 1706 1332 2004"> <thead> <tr> <th>Size <math>\Phi</math></th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.20</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.50</math></td> <td>3</td> </tr> <tr> <td><math>0.50 &lt; \Phi \leq 1.00</math></td> <td>2</td> </tr> <tr> <td><math>1.00 &lt; \Phi</math></td> <td>0</td> </tr> <tr> <td>Total Q TY</td> <td>3</td> </tr> </tbody> </table>	Size $\Phi$	Acceptable Q TY	$\Phi \leq 0.20$	Accept no dense	$0.20 < \Phi \leq 0.50$	3	$0.50 < \Phi \leq 1.00$	2	$1.00 < \Phi$	0	Total Q TY	3	2.5
Size $\Phi$	Acceptable Q TY															
$\Phi \leq 0.20$	Accept no dense															
$0.20 < \Phi \leq 0.50$	3															
$0.50 < \Phi \leq 1.00$	2															
$1.00 < \Phi$	0															
Total Q TY	3															



NO	Item	Criterion	AQL																		
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination																			
06	Chipped glass	<p>Symbols Define:  x: Chip length      y: Chip width      z: Chip thickness  k: Seal width      t: Glass thickness      a: LCD side length  L: Electrode pad length:</p> <p>6.1 General glass chip :  6.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="443 929 1353 1137"> <thead> <tr> <th>z: Chip thickness</th> <th>y: Chip width</th> <th>x: Chip length</th> </tr> </thead> <tbody> <tr> <td><math>Z \leq 1/2t</math></td> <td>Not over viewing area</td> <td><math>x \leq 1/8a</math></td> </tr> <tr> <td><math>1/2t &lt; z \leq 2t</math></td> <td>Not exceed 1/3k</td> <td><math>x \leq 1/8a</math></td> </tr> </tbody> </table> <p>⊙If there are 2 or more chips, x is total length of each chip.</p> <p>6.1.2 Corner crack:</p>  <table border="1" data-bbox="443 1570 1353 1778"> <thead> <tr> <th>z: Chip thickness</th> <th>y: Chip width</th> <th>x: Chip length</th> </tr> </thead> <tbody> <tr> <td><math>Z \leq 1/2t</math></td> <td>Not over viewing area</td> <td><math>x \leq 1/8a</math></td> </tr> <tr> <td><math>1/2t &lt; z \leq 2t</math></td> <td>Not exceed 1/3k</td> <td><math>x \leq 1/8a</math></td> </tr> </tbody> </table> <p>⊙If there are 2 or more chips, x is the total length of each chip.</p>	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	2.5
z: Chip thickness	y: Chip width	x: Chip length																			
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$																			
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z: Chip thickness	y: Chip width	x: Chip length																			
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NO	Item	Criterion	AQL																
06	Glass crack	<p>Symbols :</p> <p>x: Chip length      y: Chip width      z: Chip thickness</p> <p>k: Seal width      t: Glass thickness      a: LCD side length</p> <p>L: Electrode pad length</p> <p>6.2 Protrusion over terminal :</p> <p>6.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="352 808 1275 909"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td><math>y \leq 0.5\text{mm}</math></td> <td><math>x \leq 1/8a</math></td> <td><math>0 &lt; z \leq t</math></td> </tr> </table> <p>6.2.2 Non-conductive portion:</p>  <table border="1" data-bbox="424 1290 1251 1406"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td><math>y \leq L</math></td> <td><math>x \leq 1/8a</math></td> <td><math>0 &lt; z \leq t</math></td> </tr> </table> <ul style="list-style-type: none"> <li>⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</li> <li>⊙ If the product will be heat sealed by the customer, the alignment mark not be damaged.</li> </ul> <p>6.2.3 Substrate protuberance and internal crack.</p>  <table border="1" data-bbox="762 1711 1323 1812"> <tr> <td>y: width</td> <td>x: length</td> </tr> <tr> <td><math>y \leq 1/3L</math></td> <td><math>x \leq a</math></td> </tr> </table>	y: Chip width	x: Chip length	z: Chip thickness	$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$	y: Chip width	x: Chip length	z: Chip thickness	$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$	y: width	x: length	$y \leq 1/3L$	$x \leq a$	2.5
y: Chip width	x: Chip length	z: Chip thickness																	
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y: width	x: length																		
$y \leq 1/3L$	$x \leq a$																		

NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit.	0.65
		8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards.	2.5
		8.3 Backlight doesn't light or color wrong.	0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.	2.5
		9.2 Bezel must comply with job specifications.	0.65
10	PCB、COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination.	2.5
		10.2 COB seal surface may not have pinholes through to the IC.	
		10.3 The height of the COB should not exceed the height indicated in the assembly diagram.	2.5
		10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.	0.65
		10.5 No oxidation or contamination PCB terminals.	2.5
		10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.	0.65
		10.7 The jumper on the PCB should conform to the product characteristic chart.	
		10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down.	0.65
		10.9 The Scraping testing standard for Copper Coating of PCB	2.5
		 <p><math>X * Y \leq 2\text{mm}^2</math></p>	2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB.	2.5
		11.2 No cold solder joints, missing solder connections, oxidation or icicle.	2.5
		11.3 No residue or solder balls on PCB.	2.5
		11.4 No short circuits in components on PCB.	0.65

NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	0.65
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	0.65
		12.12 Visual defect outside of VA is not considered to be rejection.	

# 12. Material List of Components for RoHs

1. WINSTAR Display Co., Ltd hereby declares that all of or part of products (with the mark “#”in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A : The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm
Above limited value is set up according to RoHS.						

2. Process for RoHS requirement :

- (1) Use the Sn/Ag/Cu soldering surface ; the surface of Pb-free solder is rougher than we used before.
- (2) Heat-resistance temp. :
  - Reflow : 250°C ,30 seconds Max. ;
  - Connector soldering wave or hand soldering : 320°C , 10 seconds max.
- (3) Temp. curve of reflow, max. Temp. : 235±5°C ;
  - Recommended customer’s soldering temp. of connector : 280°C , 3 seconds.

# **13.Recommendable Storage**

1. Place the panel or module in the temperature  $25^{\circ}\text{C}\pm 5^{\circ}\text{C}$  and the humidity below 65% RH
2. Do not place the module near organics solvents or corrosive gases.
3. Do not crush, shake, or jolt the module.



**1、Panel Specification :**

- 1. Panel Type :  Pass  NG , \_\_\_\_\_
- 2. View Direction :  Pass  NG , \_\_\_\_\_
- 3. Numbers of Dots :  Pass  NG , \_\_\_\_\_
- 4. View Area :  Pass  NG , \_\_\_\_\_
- 5. Active Area :  Pass  NG , \_\_\_\_\_
- 6. Operating Temperature :  Pass  NG , \_\_\_\_\_
- 7. Storage Temperature :  Pass  NG , \_\_\_\_\_
- 8. Others : \_\_\_\_\_

**2、Mechanical Specification :**

- 1. PCB Size :  Pass  NG , \_\_\_\_\_
- 2. Frame Size :  Pass  NG , \_\_\_\_\_
- 3. Material of Frame :  Pass  NG , \_\_\_\_\_
- 4. Connector Position :  Pass  NG , \_\_\_\_\_
- 5. Fix Hole Position :  Pass  NG , \_\_\_\_\_
- 6. Backlight Position :  Pass  NG , \_\_\_\_\_
- 7. Thickness of PCB :  Pass  NG , \_\_\_\_\_
- 8. Height of Frame to PCB :  Pass  NG , \_\_\_\_\_
- 9. Height of Module :  Pass  NG , \_\_\_\_\_
- 10. Others :  Pass  NG , \_\_\_\_\_

**3、Relative Hole Size :**

- 1. Pitch of Connector :  Pass  NG , \_\_\_\_\_
- 2. Hole size of Connector :  Pass  NG , \_\_\_\_\_
- 3. Mounting Hole size :  Pass  NG , \_\_\_\_\_
- 4. Mounting Hole Type :  Pass  NG , \_\_\_\_\_
- 5. Others :  Pass  NG , \_\_\_\_\_

**4、Backlight Specification :**

- 1. B/L Type :  Pass  NG , \_\_\_\_\_
- 2. B/L Color :  Pass  NG , \_\_\_\_\_
- 3. B/L Driving Voltage (Reference for LED Type) :  Pass  NG , \_\_\_\_\_
- 4. B/L Driving Current :  Pass  NG , \_\_\_\_\_
- 5. Brightness of B/L :  Pass  NG , \_\_\_\_\_
- 6. B/L Solder Method :  Pass  NG , \_\_\_\_\_
- 7. Others :  Pass  NG , \_\_\_\_\_

>> Go to page 2 <<



**5、Electronic Characteristics of Module :**

- 1. Input Voltage :  Pass  NG , \_\_\_\_\_
- 2. Supply Current :  Pass  NG , \_\_\_\_\_
- 3. Driving Voltage for LCD :  Pass  NG , \_\_\_\_\_
- 4. Contrast for LCD :  Pass  NG , \_\_\_\_\_
- 5. B/L Driving Method :  Pass  NG , \_\_\_\_\_
- 6. Negative Voltage Output :  Pass  NG , \_\_\_\_\_
- 7. Interface Function :  Pass  NG , \_\_\_\_\_
- 8. LCD Uniformity :  Pass  NG , \_\_\_\_\_
- 9. ESD test :  Pass  NG , \_\_\_\_\_
- 10. Others :  Pass  NG , \_\_\_\_\_

**6、Summary :**

Sales signature : \_\_\_\_\_

Customer Signature : \_\_\_\_\_

Date :    /    /